Code: 20EC3403

II B.Tech - II Semester – Regular / Supplementary Examinations MAY - 2023

MICROPROCESSOR & MICROCONTROLLERS (ELECTRONICS & COMMUNICATION ENGINEERING)

Duration: 3 hours Max. Marks: 70

Note: 1. This paper contains questions from 5 units of Syllabus. Each unit carries 14 marks and have an internal choice of Questions.

2. All parts of Question must be answered in one place.

BL – Blooms Level CO – Course Outcome

			BL	СО	Max.			
					Marks			
UNIT-I								
1	a)	Explain Princeton and Harvard architectures	L2	CO1	7 M			
		used in processors in detail.						
	b)	Outline the evolution of 4 bit to 32 bit	L2	CO2	7 M			
		microcontrollers.						
OR								
2	a)	Explain in detail about CISC and RISC	L2	CO1	7 M			
		systems.						
	b)	Explain Memory Latency, Cache Memory	L3	CO2	7 M			
		and its significance in computer design.						
UNIT-II								
3	a)	What is an addressing mode? Explain	L3	CO2	7 M			
		various addressing modes of 8086 along						
		with examples.						
	b)	Explain how the pipelined architecture is	L3	CO2	7 M			
		implemented in 8086.						
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		OR						
4	a)	Explain the maximum mode pins of 8086	L2	CO2	7 M			
		microprocessor.						
	b)	Sketch read and write timing diagrams in	L3	CO2	7 M			
		minimum mode configuration of 8086						
		microprocessor.						
UNIT-III								
5	a)	Describe the Registers and its functionality	L2	CO2	7 M			
		of MSP430 microcontroller.						
	b)	Make use of Low Power applications in	L3	CO3	7 M			
		MSP430 microcontroller.						
		OR						
6	a)	Identify the practices for Low-Power	L3	CO3	7 M			
		Consumption with an example.						
	b)	Illustrate the memory map of MSP430	L3	CO3	7 M			
		microcontroller.						
UNIT-IV								
7	a)	Illustrate the Watch Dog Timer interrupt of	L3	CO4	7 M			
		MSP430 microcontroller.						
	b)	Interface LCD with MSP430	L3	CO4	7 M			
		microcontroller.						
	OR							
8	a)	Make use of Timers in MSP430	L3	CO4	7 M			
		microcontroller.						
	b)	Explain the DMA Control Interrupts and	L3	CO4	7 M			
		DMA Registers in MSP430 microcontroller.						

UNIT-V								
9	a)	Analyze Arithmetic Instructions in MSP430	L4	CO5	7 M			
		microcontroller with examples.						
	b)	Outline the differences between Single	L4	CO5	7 M			
		Operand Core Instructions and Double						
		Operand Core Instructions with relevant						
		examples.						
OR								
10	a)	Test various Data Instructions in MSP430	L4	CO5	7 M			
		microcontroller.						
	b)	Interpret the use of Shift and Rotate	L3	CO5	7 M			
		Operations and Decimal Arithmetic in						
		MSP430 microcontroller with suitable						
		examples.						